

- linked to one another and having at least one sequence of execution,comprising the following steps:
 - a. storing in each of said first sequence of modules a skip value indicating a next module in said sequence of modules to execute;
 - b. executing a first module of said first sequence of said modules; and
 - c. executing said next module of said sequence of modules indicated by the skip value, wherein each module of said sequence of modules comprises at least one digital signal processing data structure.

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(Twice amended) A method of controlling execution flow of a first task comprising a sequence of first executable modules in a processing system by storing in each of said first executable modules a skip count, said skip count comprising an integer N which indicates that execution should skip to the N+1th module following execution of a currently executed module in the first sequence of executable modules, a value of N less than zero associated with the currently executed module indicating that execution of the first sequence of modules should terminate after completion of execution of the currently executed module wherein each module comprises at least one digital signal processing data structure.

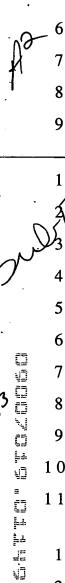
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(Amended) A method performed by a processor of controlling the flow of execution of a first set of executable modules sequentially associated with one another comprising the following steps:

- a. executing a first module in said first sequence of modules;
- b. determining a skip value associated with said first module; and

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proceeding to execute a subsequent module in said first set of c. executable modules indicated by said skip value, wherein each module comprises at least one digital signal processing data

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(Amended) An apparatus for executing a first sequence of modules in a first task, said first sequence of modules linked to one another and having at least one sequence of execution, comprising:

- means for storing in each of said first sequence of modules a skip a. value indicating a next module in said sequence of modules to execute;
- b. means for executing a first module of said first sequence of said modules; and
- means for executing said next module of said sequence of c. modules indicated by the Kip value, wherein each module comprises at least one digital signal processing data structure.

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17. (Amended) An apparatus for controlling the flow of execution of a first set of executable/modules sequentially associated with one another comprising:

- a. means for executing a first module in said first sequence of modules;
- b. means for determining a skip value associated with said first module; and
- c. means for proceeding to execute a subsequent module in said first set of executable modules indicated by said skip value,





wherein each module comprises at least one digital signal processing data structure.

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18.

(Twice amended) A method of controlling the execution sequence of a series of modules by a processor, each of said modules associated with one another, comprising the following steps:

- executing the first in said series of modules; a.
- b. determining a skip value N stored in said first in said series of said modules:
- if the skip value N stored in said first module is less than zero, c. then terminating the execution of said series of modules;
- d. else if the skip value N stored in said/first module is greater than or equal to zero then proceeding to/a N+1th module in said series of said modules, wherein each of said modules comprises at least one digital signal processing data structure.

19. (Amended) A method in a computer system of performing a first sequence of modules in a first task, said first sequence of modules linked to one another and having at least one sequence of execution, comprising the following steps:

- a. storing in a first module of said first sequence of modules a skip value N representing a subsequent module in said first sequence of modules to execute, said skip value N comprising either:
 - i. an integer less than zero indicating that said first module is a last executable module to be executed in said sequence of modules;





| | e subsequent |
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| 1 2 process should proceed to said N+1th module | |
| to said first module in said first sequence of sa | aid modules; |
| 14 b. executing the first of said first sequence of said modu | ules; and |
| 15 c. executing the subsequent module in said sequence of | of said |
| 16 modules indicated by said skip value, wherein each i | module of |
| 17 <u>said sequence of modules comprises at least one digit</u> | ital signal |
| · 18 processing data structure. | |